

ABSTRACT OF THE DISCLOSURE

A photoreceptor is provided with an erroneous operation detection circuit for monitoring an output signal supplied from a slew rate variable output stage. The erroneous operation detection circuit performs such controls that a slew rate of output is decreased within a range in which a transmission speed of the slew rate variable output stage is not decreased. With this arrangement, it is possible to inhibit adverse effect on the output signal due to a parasite current caused between an output terminal of the photoreceptor and photoelectric converting means, such as a photodiode.